ATHARVA EDUCATIONAL TRUST'S

ATHARVA COLLEGE OF ENGINEERING

(Approved by AICTE, Recognized by Government of Maharashtra &

Affiliated   to University of Mumbai – Estd. 1999 - 2000)

ISO 2100:2018 ISO 14001: 2015 ISO 9001: 2015, NAAC Accredited

Academic Year 2023-24 (Odd Semester)

Department of EXTC & ECS

ACE/EXTC, ECS/FR/20/2023-24
DATE: 14/07/2023

Report of Workshop for Faculty 2023-24

Event Name:	Two Days Workshop
College/Institute Name:	Atharva College of Engineering
Date and Time:	13th & 14th July 2023, at 9:30 AM onwards
Venue:	Lab 8A, 4th Floor, Phase 1, ACE
Speaker Name and Designation	Mr. Shrikant Atkarne, Senior Application Engineer, Spaartasoft Technology Solutions.
Topic of Event:	**"Hands on Training in VLSI using Microwind Tool"**
Mode of conduction:	Offline mode
Coordinators of the Event:	Ms. Mahalaxmi Palinje, Ms. Ruchi Chauhan, Ms.Surabhi Laharia & Ms. Prajakta Borole
Targeted Audience:	Faculty of EXTC and ECS Department
No. of Participants:	25
Brief Description of Event:	**Objective of the Event:** 1. Introduction to VLSI Design & Career scope 2. Introduction to Microwind software tool 3. Hands-on training on Microwind tool for layout designing 4. Running overview on various experiments to be conducted in DVLSI, EDC, DSD subjects **Topics Covered in the Event:** Day 1: 1. The first day of the workshop started with a motivational speech by Dr. Bhavin Shah , HOD EXTC Dept. & Dr. Kavita Bani HOD ECS Dept. 2. Mr. Shrikant Atkarne explained the basic concepts needed for workshop and the use of software in VLSI domain 3. Hands-on training on Microwind software tool was conducted to have the practical exposure of the CMOS layout design 4. Right from the installation of the software to the end design of the layout, Mr. Shrikant Atkarne guided faculties phenomenally. 5. Day 1 of the workshop ended with faculties acquiring the basic knowledge of the layout design using Microwind software.

Address: Malad-Marve Road, Charkop Naka, Malad (W), Mumbai 400095, Maharashtra, India

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	Day 2:
	1. Second day, Prof. Mahalaxmi Palinje gave Hands-on training on DSCH software to design and implement different digital circuits experience. She taught the rules to design layouts using the Microwind tool.
	2. At the end of the Hand-on training, a Question-and-Answer session was conducted to solve all the doubts.
	Outcome of the Event:
	1. Faculties had practical exposure to the layout designing using Microwind software tool and Circuit schematic designing using DSCH software
	2. Faculties also had interactive session with Mr. Shrikant Atkarne who is expert in VLSI domain.

Photographs of the Event :

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Prof. Mahalaxmi Palinje
Prof. Ruchi Chauhan
EXTC Dept Coordinator, ACE

Prof. Surabhi Laharia
Prof. Prajakta Borole
ECS Dept Coordinator, ACE

Dr. Bhavin Shah
HOD EXTC, ACE

Dr. Kavita Bani
HOD ECS, ACE

Dr. Ramesh Kulkarni
Principal, ACE

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