



Academic Year 2021-22 (ODD SEM)
EXTC Department

Class Wise Time-Table SE

DAY	CLASS	Batch	9-11 am.	11.20 TO 12.20	12.25 To 1.25	2 TO 3	3.10 TO 4.10	Batch	4.15 TO 6.15
MONDAY	SE	A	SL(JD)					A	
		B					B	DSD(AM)	
		C					C	EDC (MP)	
		D					D	DSD(DS)	
		E	EIC(GS)				E		
		F		NT (JG)		DSD (HM)	EIC(SM)	F	
TUESDAY	SE	A	EIC(SD)					A	
		B					B		
		C	EIC(SM)				C		
		D					D	EDC (MP)	
		E					E	DSD(HM)	
		F	EDC(JG)	NT(JG)	AM-III TUT(CG)	DSD (HM)	EIC(SM)	F	SL(PP)
WEDNESDAY	SE	A	EIC(SM)					A	DSD(HM)
		B					B	SL(PP)	
		C					C	SL(RC)	
		D	SL(JD)				D		
		E	EDC(JG)				E		
		F		MPL (JG/AM/VR/DS)	AM-III(CG)	NT(RC)	EDC(MP)	F	DSD(DS)
THURSDAY	SE	A						A	
		B					B		
		C					C		
		D					D		
		E					E		
		F			AM-III(CG)	NT(RC)	EDC(MP)	F	EIC (Lec) (4.15-5.15PM)(SM)
FRIDAY	SE	A	EDC(JG)	MPL (JG/AM/VR/DS)				A	
		B	EDC(MK)				B		
		C					C	DSD(AM)	
		D	EIC(GS)				D		
		E					E	SL(RC)	
		F	EIC(SD)		AM-III(CG)	DSD (HM)	EDC(MP)	F	

MPL- Mini Project Lab
 SE ET1 Class Incharge : Prof. Shikha Malik
 SE ET2 Class Incharge: Prof. Jyoti Gurav

Class Wise Time-Table TE

DAY	CLASS	9-10	10.10-11.10	11.20-12.20	12.25-1.25	2-3	3.10-4.10	Batch	4.15-6.15
MONDAY	TE	RSA(VR)	DC (SD)	DTSP (JD)	MPL (JG/AM/DS/RC/SG/GV)	Micro controllers & processors (JG)	DCC (DP)/ITS(JK)/DSA (AM)	A	DC(SD)
								B	
								C	PCE-II (RM)
								D	DVLSI(HM)
								E	DTSP(JG)
								F	DC(SM)
TUESDAY	TE	RSA(VR)	MPL(JG/AM/DS/RC/SG/GV)	DTSP (JD)	DVLSI(DS)	Network Analysis (DP)	DCC (DP)/ITS(JK)/DSA (AM)	A	DTSP(GS)
								B	DVLSI(DS)
								C	
								D	PCE-II (RM)
								E	DTSP(JD)
								F	PCE-II (RM)
WEDNESDAY	TE	RSA(VR)	DC (SD)	PCE-II (RM) Batch E			Instrumentation & control systems(SM)	A	DTSP(GS)
								B	
								C	DC(SD)
								D	
								E	
								F	
THURSDAY	TE						DCC (DP)/ITS(JK)/DSA (AM)	A	Basic electronics(GS)(4.15-5.15)
								B	
								C	PCE-II (RM)
								D	DVLSI(DS)
								E	DTSP(JD)
								F	DC(SM)
FRIDAY	TE	PCE-II (RM)	DVLSI(DS)	DC (SD)	Op amp & circuits(RC)	Basics of antenna(AM)	MPL(JG/AM/DS/RC/SG/GV)	A	Basic microprocessors(HM)(5.15-6.15)
								B	
								C	PCE-II (RM)
								D	DVLSI(DS)
								E	DTSP(JD)
								F	DC(SM)

MPL- Mini Project Lab
 TE ET1 Class Incharge : Prof. Divya Sharma
 TE ET2 Class Incharge: Prof. Dhanashree Pannase

Class Wise Time-Table BE

DAY	CLASS	9-10	10.10-11.10	11.20 TO 12.20	12.25 To 1.25	2 TO 3	3.10 TO 4.10	BATCH	4.15 TO 6.15
MONDAY	BE	OC (GV)	MCS(MK)	Electro magnetics theory (VR)	ME(PP)	Basic electronics(MP)	BDA(JM)	A	MCS(MK)
								B	ME(PP)
								C	
								D	BDA(RC)
								E	
								F	ME(DP)
TUESDAY	BE	OC (GV)	MCS(MK)	Digital circuits(DS)	ME(PP)	Network Analysis (JG)	BDA(JM)	A	OC(GV)
								B	MCS(AM)
								C	ME(PP)
								D	OC(JSG)
								E	BDA(RC)
								F	BDA(VR)
WEDNESDAY	BE	Basics of microwave (MK)	MCS(MK)	IE (GS/PN/SZ)	ME(PP)	OC (JSG)	BDA(JM)	A	BDA(JM)
								B	OC(GV)
								C	MCS(MK)
								D	ME(DP)
								E	OC(JSG)
								F	MCS(AM)
THURSDAY	BE	Digital signal processing(JD)	MCS(MK)	IE (GS/PN/SZ)	ME(PP)	Basic Digital communications(SD)	BDA(JM)	A	ME(PP)
								B	BDA(RC)
								C	OC(GV)
								D	MCS(MP)
								E	ME(DP)
								F	OC(JSG)
FRIDAY	BE	Basics of optical communication (GV)	OC (JSG)	IE (GS/PN/SZ)	Basic microwave engg(PP)		BE Project	A	
								B	
								C	BDA(VR)
								D	
								E	MCS(MP)
								F	

MPL- Mini Project Lab
 BE ET1 Class Incharge : Mohan Kumar
 BE ET2 Class Incharge : Gauri Vaidya

Subject Incharges

JM: Jyoti Mali	JD: Jyoti Dange
MP: Mahalaxmi Palanje	RC: Ruchi Chauhan
JG: Jyoti Gurav	SD: Supriya Dicholkar
SM: Shikha Malik	VR: Vivek Ramkrishnan
HM: Hemalata Mote	DP: Dhanashree Pannase
ML: Mohan Kumar	RM: Rajendra Mahajan
GV: Gauri Vaidya	PP: Prajakta Pawar
CG: Chanchal Goradia	GS: Gauri Salunkhe
	JSG: Joslyn Gracias
AM: Anicy Misquitto	DS: Divya Sharma

Prof. Mahalaxmi Palanje
 Incharge HOD EXTC

Ms. Gauri Salunkhe / Ms. Shikha Malik
 Time table Incharge

Dr. S.P Kallurkar
 Principal



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