



ATHARVA EDUCATIONAL TRUST'S
ATHARVA COLLEGE OF ENGINEERING

(Approved by AICTE, Recognized by Government of Maharashtra
& Affiliated to University of Mumbai - Estd. 1999 - 2000)
Academic Year 2017-18

Department of Electronics and Telecommunication Engineering

MASTER TIMETABLE 2017-18 EVEN SEM

	CLASS	9 TO 10	10 TO 11	11 TO 12	12 TO 1	1 TO 2	2 TO 3	3 TO 4	4 TO 5	
M O N D A Y	SE ET 1	AM IV (GJS)	PCOM (RS)	SS (DS)	L	S11 PCOM LAB 2A, S12 SS LAB 7, S13 LIC LAB 3		LIC (GS)	EDC II (DSH)	R8
	SE ET 2	S 21 EDC II LAB 3, S22 SS LAB 7, S23 LIC LAB 4		SS (SJ)	LIC (MM)	L	EDC II (MP)	AM IV (GJS)	PCOM (PV)	R9
	TE ET 1	TVE (MK)	DCOM (KS)	CCN (JG)	OS (AB)	L	DTSP (JD)	T11 CEL III LAB 8A, T12 CEL IV LAB8B, T13 DTSP ELEC LAB3		R10
	TE ET 2	CCN (BS)	OS (AB)	T21 CEL III LAB 8A, T22 CEL IV LAB 8B, T23 DTSP LAB 3		L	TVE (MK)	DTSP (PM)	NPTEL JG LAB3	R11
	BE ET 1	TNM (JM) (R9)	WN (NG) (R9)	SCOM (MS) (R11)	L	B11 WN LAB 8B, B12 SCOM LAB 1, B13 TNM LAB 8A		WN (NG) (R10)		
	BE ET 2	PROJECT DAY								
T U E S D A Y	SE ET 1	S 11 SS LAB7, S12 EDCII LAB 4, S13 PCOM lab2A		PCOM (RS)	LIC (GS)	L	SS (DS)	TNP DS (R8)	AM IV (GJS)	R8
	SE ET 2	EDC II (MP)	SS (SJ)	AM IV (GJS)	PCOM (PV)	L	EDC II(MP)	S21 PCOM lab 2A, S22 LIC LAB 3, S23 SS LAB 7		R9
	TE ET 1	DTSP (JD)	VLSI (TS)	T11 DTSP ELEC LAB 3, T12 CEL III LAB 8A, T13 CEL IV LAB 8B		L	OS (AB)	CCN (JG)	TVE (MK)	R10
	TE ET 2	CCN (BS)	DCOM(DP)	OS (AB)	VLSI (TS)	L	TVE (MK)	MINI PROJECT MS LAB 8A		R11
	BE ET 1	B11 INVC LAB 8A, B12 WN LAB 8B, B13 SCOM LAB1		INVP (JK) (R10)	SCOM (MS) (R10)	L	TNP SD LAB 2	TNM (JM) (R11)		
	BE ET 2	WN (NG) (R8)	SATCOM (MS) (R8)	TNP PP LAB 5	L	B21 WN LAB 8A, B22 SCOM LAB 1, B23 TNM LAB 3		TNM (PP) (R9)	INVP (SD) (R9)	
W E D N S D A Y	SE ET 1	SS (DS)	AM IV TUT(GJS)	LIC (GS)	L	PCOM (RS)	AM IV (GJS)	NPTEL DS ELEC LAB 3		R8
	SE ET 2	S 21 LIC LAB3, S 22 PCOM lab2A, S 23 EDC II LAB4		SS (SJ)	LIC (MM)	L	PCOM (PV)	AM IV (GJS)	NPTEL MM ELEC LAB 3	R9
	TE ET 1	DTSP (JD)	CCN (JG)	TVE (MK)	L	T11 CEL IV LAB 8B, T12 DTSP ELEC LAB 3, T13 CEL III LAB 8A		OS (AB)	DCOM (KS)	R10
	TE ET 2	T21 CEL IV LAB8B, T22 DTSP ELEC LAB3, T23 CEL III LAB8A		CCN (BS)	VLSI (TS)	L	OS (AB)	DCOM (DP)	VLSI (TS)	R11
	BE ET 1	TNM(JM) (R9)	INVC (JK) (R9)	B11 SCOM LAB 1, B12 TNM LAB 8A, B13 INVC LAB 8B		SCOM (MS) (R9)	L	WN (NG) (R8)	INVC (JK) (R8)	
	BE ET 2	PP TNM (R11)	INVP(R11)(SD)	TNP PP LAB 5	WN (NG) (R8)	L	TNM (PP) (R10)	B21 INVC LAB 3, B22 WN LAB 8A, B23 SCOM LAB 1		
T H U R S D A Y	SE ET 1	S 11 LIC LAB3, S12 PCOM lab2A, S13 EDC II LAB4		EDC II (DSH)	L	AM IV (GJS)	PCOM (RS)	LIC (GS)	TNP RS LAB5	R8
	SE ET 2	LIC(MM)	EDC II(MP)	SS (SJ)	L	S 21 SS LAB7, S 22 EDC II LAB 4, S 23 PCOM lab2A		TNP PV LAB5		R9
	TE ET 1	NPTEL JG LAB8A	DTSP (JD)	OS (AB)	CCN (JG)	L	DCOM (KS)	VLSI (TS)	TNP PM LAB2	R10
	TE ET 2	DTSP (PM)	TVE (MK)	TNP SD LAB 5	OS (AB)	L	VLSI (TS)	DTSP (PM)	DCOM (DP)	R11
	BE ET 1	WN (NG) (R8)	B11 TNM LAB8A, B12 INVC LAB8B, B13 WN ELEC LAB3		L	TNM (JM) (R9)	TNP MK LAB 5	INVC (JK) (R9)	SCOM (MS) (R9)	
	BE ET 2	SCOM (MS) (R10)	INVP (SD) (R10)	SCOM (MS) (R11)	L	TNM (PP) (R10)	B21 TNM LAB 8A, B22 INVC LAB 3, B23 WN LAB 8B			

Prof. Akanksha Bhargawa/ Prof. Pooja Sonawane
Time Table Incharge

Prof. Jyoti Kolap
HOD EXTC

Dr. S.P Kallurkar
Principal, ACE

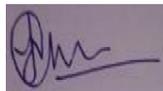


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F R I D A Y	SE ET 1	S 11 EDCII, S12 LIC LAB 4 , S13 SS LAB 7		EDC II (DSH)	L	SS (DS)	EDC II (DSH)	AM IV TUT (GJS)	R8	
	SE ET 2	AM IV TUT (GJS)	AM IV (GJS)	PCOM (PV)	AM IV TUT (GS)	L	LIC(MM)	TNP PV LAB 5	R9	
	TE ET 1	DCOM (KS)	VLSI (TS)	VLSI (TS)	L	TVE (MK)	MINI PROJECT SJ LAB4 & ELEC LAB 3		TNP PM LAB5 R10	
	TE ET 2	CCN (BS)	DTSP(PM)	TVE (MK)	DCOM (DP)	L	T21 DTSP LAB3, T22 CEL III LAB8A, T23 CEL IV LAB8B		TNP RS LAB 2 R11	
	BE ET 1	PROJECT DAY								
	BE ET 2	INVC (SD) (R8)	B21 SCOM LAB 1, B22 TNM LAB 8B, B23 INVC ELEC LAB 3		WN (NG) (R8)	L	SCOM (MS) (R11)	WN (NG) (R11)		



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