



**ATHARVA EDUCATIONAL TRUST'S
ATHARVA COLLEGE OF ENGINEERING**

(Approved by AICTE (R12), Recognized by Government of Maharashtra
& Affiliated to University of Mumbai - Estd. 1999 - 2000)

Department of Electronics Engineering

Master Time Table											
Department: ETRX		Sem: Odd Year: 2019-20		Prepared By: Prajakta Borole & Namarata Lade		Date: 11.07.2019		Academic Year: 2019-20		FR/ETRX/JUL19/04	
DAY/TIME	Class	9-10	10-11	11-12	12-1	1-2	2-3	3-4	4-5		
Monday	SE	ENAM-(B1)-(BS)-LAB A DCD-(B2)-(SK)-LAB B & C		EDC- I (PB)-R18	DCD (SK)-R18	LUNCH	EIM-(NS) R18	EDC- I (B1)-(PB)-LAB A & C OOPS -(B2)-(IT)-LAB B			
	TE	DCOM-(KB)-R1 7	DBMS-(SJ)-R17 ASIC-(AB)-LAB D	MCA-(SZ)-R17	LUNCH	MCA-(B1)-(SZ)- LAB A DCOM-(B2)-(SJ)- LAB C BCE-(B3)-(AM)- LAB B		EME-(SK)-R17	DLIC-(BK)-R17		
	BE	PE-(BK)-R18	DSPP-(NS)-R18	PE-(B2)-(BK)-LAB C DSP-(B3)-(NS)-LAB B ISD-(B1)-(AB)-LAB A		LUNCH	ISD-(AB)-R17	ICT-(KB)-R18 ANT-(AJ)-LAB D	ISD-(AB)-R18		
TUESDAY	SE	ENAM-(B2)-(BS)-LAB A DCD-(B3)-(SK)-LAB B & C		ENAS (BS)-R18	MATHS (NG)-R18	LUNCH	DCD (SK)-R18	EDC- I (B2)-(PB)-LAB A & C OOPS -(B3)-(IT)-LAB B			
	TE	DLIC-(BK)-R17	DBMS-(SJ)-R17 ASIC-(AB)-LAB D	EME-(SK)-R17	LUNCH	MCA-(B2)-(SZ)- LAB A DCOM-(B3)-(SJ)- LAB C BCE-(B1)-(AM)- LAB B		DCOM-(KB)-R1 7	MCA-(SZ)-R17		
	BE	ICT-(KB)-R18 ANT-(AJ)-LAB D	DM-(SZ)-R18	PE-(B3)-(BK)-LAB C DSP-(B1)-(NS)-LAB B ISD-(B2)-(AB)-LAB A		LUNCH	ISD-(AB)-R17	DSPP-(NS)-R18	PE-(BK)-R18		
WEDNESDAY	SE	ENAM-(B3)-(BS)-LAB A DCD-(B1)-(SK)-LAB B & C		DCD (SK)-R18	LUNCH	MATHS (NG)-R18	EDC- I (PB)-R18	EDC- I (B3)-(PB)-LAB A & C OOPS -(B1)-(IT)-LAB B			
	TE	DCOM-(KB)-R1 7	DBMS-(SJ)-R17 ASIC-(AB)-LAB D	DCOM-(KB)-R17	LUNCH	MCA-(B3)-(SZ)- LAB A DCOM-(B1)-(KB)- LAB C BCE-(B2)-(AM)- LAB B		EME-(SK)-R18	EME-TUT-(SK)-R1 8		
	BE	PE-(BK)-R18	DSPP-(NS)-R18	DM-(SZ)-R6	LUNCH	DSPP-(NS)-R17	ISD-(AB)-R17	PE-(BK)-R17	-----		
THURSDAY	SE	ENAS (BS)-R18	ENAS (BS)-R18	EIM-(NS) R18	LUNCH	OOPS-(IT)- R18	EDC- I (PB)-R18	MATHS (NG)-R18	MATHS- TUT (NG)-R18		
	TE	DLIC-(B2)-(NS)-LAB C DBMS-(B3)-(SJ)-LAB B --- TNP-(B1)-(SZ)-LAB D		DBMS-(SJ)-R17 ASIC-(AB)-LAB D	LUNCH	EME-(SK)-R17	DLIC-(BK)-R17	MCA-(SZ)-R17	DLIC-(BK)-R17		





**ATHARVA EDUCATIONAL TRUST'S
ATHARVA COLLEGE OF ENGINEERING**

(Approved by AICTE (R12), Recognized by Government of Maharashtra
& Affiliated to University of Mumbai - Estd. 1999 - 2000)

Department of Electronics Engineering

	BE	ISD-(B3)-(AB)-LAB A		DM-(SZ)-R6	LUNCH	ANT-(B1)-(AJ)-LAB B ICT-(B2)-(KB)-LAB A		PROJECT DAY	
FRIDAY	SE	EIM-(NS) R18	EDC- I (PB)-R18	ENAS (BS)-R18	LUNCH	OOPS-(IT)- R18	MATHS (NG)-R18	EIM-(NS) R18	DCD (SK)-R18
	TE	DLIC-(B3)-(SZ)-LAB C ASIC-(B1)-(AB)-LAB A --- TNP-(B2)-(BS)-LAB B		MCA-(SZ)-R17	LUNCH	BCE-(AM)-R17	BCE-(AM)-R17	DLIC-(B1)-(BK)-LAB C DBMS-(B2)-(SJ)-LAB B TNP-(B3)-(SK)-LAB A -----	
	BE	ICT-(KB)-R17 ANT-(AJ)-LAB D	ICT-(KB)-R17 ANT-(AJ)-LAB D	PE-(B1)-(BK)-LAB C DSP-(B2)-(NS)-LAB B ICT-(B3)-(KB)-LAB A		PROJECT DAY			

Sr. No.	Name of Faculty	Abbreviation
1	Dr. Bhavin Shah	BS
2	Prof. Samuel Jacob	SJ
3	Prof. Prajakta Borole	PB
4	Prof. Ameya Jadhav	AJ
5	Prof. Niranjana Samudre	NS
6	Prof. Sandip Zade	SZ
7	Prof. Kavita Bani	BK
8	Prof. Kishor Bhosale	KB
9	Prof. Sarang Kulkarni	SK
10	Prof. Akansha Bhargava	AB
11	Dr. Nidhi Gupta	NG
12	Prof. Anu Madhok	AM
Sr. No	Name of Lab	Abbreviation
1	VLSI & Microprocessor Lab	Lab A
2	Digital Electronics & Network Lab	Lab B
3	Electronics & Communication	Lab C
4	Power Electronics & Measurement Lab	Lab D
5	Workshop Lab	Lab E





ATHARVA EDUCATIONAL TRUST'S
ATHARVA COLLEGE OF ENGINEERING

(Approved by AICTE (R12), Recognized by Government of Maharashtra
& Affiliated to University of Mumbai - Estd. 1999 - 2000)

Department of Electronics Engineering

Prajakta Borole
(Time Table In-charge)

Dr. Bhavin Shah
HOD (ETRX/ECS)

