



ATHARVA EDUCATIONAL TRUST'S  
**ATHARVA COLLEGE OF ENGINEERING**

(Approved by AICTE, Recognized by Government of Maharashtra  
& Affiliated to University of Mumbai - Estd. 1999 - 2000)  
Department of Electronics and Telecommunication

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## **REPORT**

**TE Orientation for department level elective subject choice 2021-22**

**Date:** 9 Dec 2021

**Organised by:** EXTC Department

**Co-Ordinators:** Prof. Gauri Salunkhe / Prof. Shikha Malik

**Google meet link:** <https://meet.google.com/etn-hpss-iwg>

**Number of attendees:** 75

The Orientation was organized on Thursday 9th December 2021 at 10 am for TE SEM VI elective subject choice.

The orientation of “**Data Base Management system**” was given by Ms. Dhaneshree Pannasee (Assistant Professor, Department of EXTC). The orientation included detailed syllabus explanation and pre-requisites with the applications of the subject in industries. Also the scope of subject in career building was discussed.

The orientation of “**IOT and Industry 4.0**” was given by Prof. Anup D Taryalkar (Assistant Professor, Department of EXTC) which included detailed syllabus explanation along with the Practicals included in the syllabus. Also the session included real life applications of IOT and Industry 4.0 in various fields like Healthcare , agriculture and automotive industry.

The orientation of “**Digital Forensic**” was given by Prof. Prof. Gauri Salunkhe (Assistant Professor, Department of EXTC). Speaker discussed the need and process of digital forensic and elaborated various types of current cyber attacks The career



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scope of the subject along with supportive certifications and Job profiles were also discussed.

The orientation of “**Mixed Signal VLSI**” was given by Prof. Mahalaxmi Palinje (Assistant Professor, Department of EXTC). Speaker discussed the importance of Mixed Signal VLSI design in the field of Electronics and Telecommunication and emerging technologies. The orientation included detailed syllabus explanation and open source tools for implementing and verifying various mixed signal VLSI circuits.

Session Screen shots:

